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DATE MAILED: 06/01/2004

APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/618,514	07/11/2003	-	Imran Khan	AMD-H0552	6596
7590 06/01/2004 WAGNER, MURABITO & HAO LLP Third Floor Two North Market Street			EXAMINER		
				KEBEDE, BROOK	
			<b>`</b> .	ART UNIT	PAPER NUMBER
San Jose, CA	95113	٠.		2823	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
Office Action Summer	10/618,514	KHAN ET AL.
Office Action Summa	Examiner	Art Unit
<del>-</del>	Brook, Kebede	2823
The MAILING DATE f this co	mmunication appears on the cover sheet w	ith the c rrespondence address
- Extensions of time may be available under the p after SIX (6) MONTHS from the mailing date of t If the period for reply specified above is less than If NO period for reply is specified above, the may Failure to reply within the set or extended period.	ovisions of 37 CFR 1.136(a). In no event, however, may a nis communication. thirty (30) days, a reply within the statutory minimum of thir imum statutory period will apply and will expire SIX (6) MON for reply will, by statute, days the application to become AE months after the mailing days the incommunication.	reply be timely filed  ty (30) days will be considered timely.  ITHS from the mailing date of this communication.
Status		
1) Responsive to communication	(s) filed on 11 July 2002	
2a) ☐ This action is FINAL.	2b)⊠ This action is non-final.	
	dition for allowance except for formal matt	
closed in accordance with the	practice under <i>Ex parte Quayle</i> , 1935 C.D	ers, prosecution as to the merits is
	produce diffici Ex parte Quayle, 1955 C.D	. 11, 453 O.G. 213.
Disposition of Claims		
4)⊠ Claim(s) <u>1-21</u> is/are pending ir	the application.	
	_ is/are withdrawn from consideration.	
5) Claim(s) is/are allowed.		
6) Claim(s) is/are rejected		
7) Claim(s) is/are objected	to.	
	striction and/or election requirement.	
Application Papers		
9)☐ The specification is objected to	by the Examiner.	
10) I he drawing(s) filed oni	s/are: a)  accepted or b)  objected to t	by the Examiner.
Applicant may not request that an	objection to the drawing(s) be held in abeyand	ce. See 37 CFR 1.85(a).
Replacement drawing sheet(s) inc	uding the correction is required if the drawing(	s) is objected to. See 37 CFR 1.121(d).
11) Ine oath or declaration is object	ted to by the Examiner. Note the attached	Office Action or form PTO-152.
Priority under 35 U.S.C. § 119		
Acknowledgment is made of a c	laim for foreign priority under 35 U.S.C. §	119(a)-(d) or (f).
a) All b) Some * c) None	·	
1. Certified copies of the pr	ority documents have been received.	والمراجع والمراجع والمحاجز والمحاجز المراجع والمحاجز والمحاجز والمحاجز والمحاجز والمحاجز والمحاجز والمحاجز
2. Certified copies of the pr	ority documents have been received in Ap	plication No
3. ☐ Copies of the certified co	pies of the priority documents have been r	eceived in this National Stage
application from the Inter	national Bureau (PCT Rule 17.2(a)).	
See the attached detailed Office	action for a list of the certified copies not re	eceived.
Attachment(s)		
1) Notice of References Cited (PTO-892)		
2) Notice of Draftsperson's Patent Drawing Revi	ew (PTO-948) Paper No(s)/	mmary (PTO-413) Mail Date
3) Information Disclosure Statement(s) (PTO-14 Paper No(s)/Mail Date	49 or PTO/SB/08) 5) D Notice of Info	ormal Patent Application (PTO-152)
U.S. Patent and Trademark Office	6)	<u>.</u>
PTOL-326 (Rev. 1-04)	Office Action Summary	Port of Donas N. W. W.

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## **DETAILED ACTION.**

## Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

Group I, Claims 1-19, drawn to Method for Fabricating a Semiconductor Device,
classified in class 438, subclass 305.

Group II, Claims 20 and 21, drawn to Semiconductor Device, classified in class 257, subclass 336+.

The inventions are distinct, each from the other because of the following reasons:

- 2. Inventions Group I and Group II are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case, the device of Group II can be manufactured by multiple doping steps to form triple LDD by forming three different polysilicon spacers on the gate and selectively doping the spacers at different doping level and performing solid state diffusion process at different energy level to diffuse the dopant form the polysilicon spacers into the substrate and thereby forming the triple LDD region in the substrate instead of forming spacer having first thickness, the second thickness, third thickness, and fourth thickness and perform an implant (i.e., single implant) to from triple LDD structure.
- 3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

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4. A telephone call was made to Mr. José S. Garcia on May 25, 2004 to request an oral election to the above restriction requirement, but did not result in an election being made.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

5. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

## Correspondence

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brook Kebede whose telephone number is (571) 272-1862. The examiner can normally be reached on 8-5 Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on (571) 272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Brook Kebede Examiner Art Unit 2823

BK May 25, 2004 Brook Kepede